

EL 440 - Communication Systems with Lab

4 Credit Hours

Introduction to characterization of inverters, concepts of delay, sizing, and power consumption in IC design, static random-access memory (SRAM) and dynamic random-access memory (DRAM) basics, with the application of electronic design automation (EDA) software Cadence/ModelSim/Vivado/Quartus, Behavioral, Dataflow and Structural Designs. The main tools to implement designs are Hardware Description Language (HDL) at a higher level and Computer Aided Design (CAD) at a lower (graphical) level will be introduced for students to practice.

Prerequisites

[EL 205](#)